



Form PTO-1449

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ANSWER KEY

TSMC-00-0688B

10/790,925

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Ta Lee Yu

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03/02/04

Exam 11b

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Page, Etc.)

J. Chen et al., "Design and Layout of a High ESD Performance NPN Structure for Submicron BiCMOS Bipolar Circuits", IEEE Jnl 1996, 0-7803-2753-5/96, pp. 227-232.

EXAMEN

DATE CONNOCTED

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.